

Electronic Acknowledgement Receipt

EFS ID:	2907419
Application Number:	09751762
International Application Number:	
Confirmation Number:	5066
Title of Invention:	Method for converting pipeline stalls caused by instructions with long latency memory accesses to pipeline flushes in a multithreaded processor
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Application Type:	Utility under 35 USC 111(a)

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Payment was successfully received in RAM	\$460

RAM confirmation Number		3991			
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The Director of the USPTO is hereby authorized to charge indicated fees and credit any overpayment as follows: Charge any Additional Fees required under 37 C.F.R. Section 1.21 (Miscellaneous fees and charges)					
File Listing:					
Document Number	Document Description	File Name	File Size(Bytes) /Message Digest	Multi Part /.zip	Pages (if appl.)
1	Amendment After Final	2207_10121_AmendmentAF.pdf	149217 <small>9d92779638309bf98b52f6d262e282e90ef110468</small>	no	13
Warnings:					
Information:					
2	Extension of Time	2207_10121_Extension.pdf	108272 <small>9d5676f2ec403462ac305d7595aecf80c766ec76</small>	no	1
Warnings:					
Information:					
3	Fee Worksheet (PTO-06)	fee-info.pdf	8233 <small>2d16172730d031687cd3c5f1deb4f433802ca35b</small>	no	2
Warnings:					
Information:					
Total Files Size (in bytes):			265722		
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